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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10021173	FILING DATE 12/19/2001	CLASS 257	SUBCLASS 709	GAU 261 284	EXAMINER <i>[Signature]</i>
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****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 2001-002371 01/10/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO H-1020
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	

TITLE : Semiconductor device and a method of manufacturing the same

U.S. DEPT. OF COMM. PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for 0.0
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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